

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	8	OZAKI adj HIDEHARU.in.	USPAT	OR	OFF	2003/09/24 17:32
S3	7	OZAKI adj HIDEHARU.in. and scan	USPAT	OR	OFF	2003/09/24 17:36
S4	88	324/76.54.ccls.	USPAT	OR	OFF	2003/09/24 17:39
S7	3	324/76.54.ccls. and phase with frequency with multipl\$5	USPAT	OR	OFF	2003/09/24 17:41
S6	3	324/76.54.ccls. and phase with frequency with multipl\$5	USPAT	OR	OFF	2003/09/24 17:41
S5	41	324/76.54.ccls. and phase	USPAT	OR	OFF	2003/09/24 17:42
S11	61	"714"/\$.ccls. and phase adj lock adj loop and scan	USPAT	OR	OFF	2003/09/24 17:43
S10	893	"714"/\$.ccls. and phase and scan	USPAT	OR	OFF	2003/09/24 17:43
S9	4963	"714"/\$.ccls. and phase	USPAT	OR	OFF	2003/09/24 17:43
S8	1	324/76.54.ccls. and phase with frequency with multipl\$5	USPAT	OR	OFF	2003/09/24 17:44
S12	102	324/76.\$2.ccls. and phase with frequency with multipl\$5	USPAT	OR	OFF	2003/09/24 17:45
S13	9	324/76.\$2.ccls. and phase adj lock with frequency with multipl\$5	USPAT	OR	OFF	2003/09/24 17:47
S14	27	324/76.\$2.ccls. and phase adj lock with frequency with multipl\$5	USPAT	OR	OFF	2003/09/24 17:48
S16	2	324/76.\$2.ccls. and phase adj lock with clock with multiply\$3	USPAT	OR	OFF	2003/09/24 17:52
S15	10	324/76.\$2.ccls. and phase adj lock with frequency with multiply\$3	USPAT	OR	OFF	2003/09/24 17:52
S17	6	semiconductor ADJ integrated ADJ circuit AND delay WITH scan ADJ path	USPAT	OR	OFF	2004/01/28 15:05
S18	8	semiconductor with integrated ADJ circuit AND delay WITH scan ADJ path	USPAT	OR	OFF	2004/01/28 15:06
S19	49	integrated ADJ circuit AND delay WITH scan ADJ path	USPAT	OR	OFF	2004/01/28 15:07
S20	5	integrated ADJ circuit with delay WITH scan ADJ path	USPAT	OR	OFF	2004/01/28 15:08
S23	231	"714"/\$.ccls. and scan adj path and register and flip-flop and integrated ADJ circuit	USPAT	OR	ON	2004/01/28 15:16
S22	28	"714"/\$.ccls. and scan adj path and register and flip-flop with delay and integrated ADJ circuit	USPAT	OR	ON	2004/01/28 15:16
S21	38	"714"/\$.ccls. and scan adj path and integrated ADJ circuit with delay	USPAT	OR	OFF	2004/01/28 15:19
S24	161	"714"/\$.ccls. and scan adj path and generator and integrated ADJ circuit	USPAT	OR	OFF	2004/01/28 15:20

S26	29	"714"/\$.ccls. and scan adj path and register and flip-flop and integrated ADJ circuit with delay	USPAT	OR	ON	2004/01/28 15:23
S27	178	"714"/\$.ccls. and scan adj path and shift adj register and flip-flop and integrated ADJ circuit	USPAT	OR	ON	2004/01/28 15:24
S28	1658	Ozaki.in.	USPAT	OR	OFF	2004/01/28 15:25
S31	1	Ozaki adj hideharu.in. and pulse and delay	USPAT	OR	ON	2004/01/28 15:36
S32	4	("4602349" "4701920" "4710931" "4825439").PN.	USPAT	OR	OFF	2004/01/28 15:37
S33	5	"5425034".URPN.	USPAT	OR	OFF	2004/01/28 15:38
S34	7	("5043986" "5132974" "5257201" "5282146" "5425034" "5513118" "5519714").PN.	USPAT	OR	OFF	2004/01/28 15:39
S35	4	("4780628" "5267247" "5271019" "5278842").PN.	USPAT	OR	OFF	2004/01/28 15:41
S30	3	Ozaki adj hideharu.in. and pulse	USPAT	OR	ON	2004/01/28 15:41
S36	17	714/726-731.ccls. and Phase adj Lock adj Loop	USPAT	OR	ON	2004/01/28 15:48
S37	237	"714"/\$.ccls. and Phase adj Lock adj Loop	USPAT	OR	ON	2004/01/28 15:49
S39	6	Ozaki adj hideharu.in. and scan adj path	USPAT	OR	OFF	2004/01/28 15:53
S29	8	Ozaki adj hideharu.in.	USPAT	OR	OFF	2004/01/28 15:53
S41	4	("4780628" "5267247" "5271019" "5278842").PN.	USPAT	OR	OFF	2004/01/28 15:54
S42	5	"5425034".URPN.	USPAT	OR	OFF	2004/01/28 15:55
S43	4	("4602349" "4701920" "4710931" "4825439").PN.	USPAT	OR	OFF	2004/01/28 15:58
S40	16	Ozaki adj hideharu.in. and scan adj path	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/01/28 16:07
S46	6	("4897837" "4942577" "5130647" "5257267" "5367551" "5378934").PN.	USPAT	OR	OFF	2004/01/28 16:14
S47	3	Ozaki adj hideharu.in. and scan adj path and generator	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/01/28 16:22
S45	1	Ozaki adj hideharu.in. and scan adj path and phase	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/01/28 16:22
S38	25	"714"/\$.ccls. and (Phase adj Lock adj Loop or PLL) and scan adj path	USPAT	OR	ON	2004/01/28 16:30

S25	14	"714"/\$.ccls. and scan adj path and pulse adj generator and integrated ADJ circuit	USPAT	OR	OFF	2004/01/29 13:04
S50	3	714/814.ccls. and scan adj path	USPAT	OR	OFF	2004/01/29 13:05
S49	3	"714"/\$.ccls. and frequency adj multiplying and scan adj path	USPAT	OR	ON	2004/10/12 11:06
S44	16	"4942577".URPN.	USPAT	OR	OFF	2004/10/12 11:10
S52	6	"4942577".URPN. and gat\$3 with clock	USPAT	OR	OFF	2004/10/12 11:18
S53	2	"4942577".URPN. and gat\$3 with scan\$3 with clock	USPAT	OR	OFF	2004/10/12 11:21
S54	211	"714"/\$.ccls. and gat\$3 with scan\$3 with clock	USPAT	OR	OFF	2004/10/12 11:22
S55	23	"714"/\$.ccls. and scan WITH gat\$3 NEAR clock	USPAT	OR	OFF	2004/10/12 11:24
S57	290	"714"/\$.ccls. and gat\$3 NEAR1 clock	USPAT	OR	OFF	2004/10/12 11:42
S56	290	"714"/\$.ccls. and gat\$3 NEAR clock	USPAT	OR	OFF	2004/10/12 11:42
S58	45	714/726-729.ccls. and gat\$3 NEAR1 clock	USPAT	OR	OFF	2004/10/12 11:43
S51	1	US-5519715-\$.DID.	USPAT	OR	OFF	2004/10/12 11:57
S59	292	"714"/\$.ccls. and two near puls\$3	USPAT	OR	ON	2005/05/09 09:56
S48	6	"714"/\$.ccls. and frequency adj multipl\$5 and scan adj path	USPAT	OR	ON	2005/05/09 09:56
S60	204	"714"/\$.ccls. and two adj puls\$3	USPAT	OR	ON	2005/05/09 09:57
S61	46	"714"/\$.ccls. and two adj puls\$3 with generat\$3	USPAT	OR	ON	2005/05/09 10:00
S62	9	"714"/\$.ccls. and two adj puls\$3 with generat\$3 and scan	USPAT	OR	ON	2005/05/09 10:03
S63	1	"714"/726-738.ccls. and two adj puls\$3 with generat\$3 and scan	USPAT	OR	ON	2005/05/09 10:04
S64	1	"714"/724-738.ccls. and two adj puls\$3 with generat\$3 and scan	USPAT	OR	ON	2005/05/09 10:11
S69	37	"714"/724-738.ccls. and AC adj test\$3 and scan	USPAT	OR	ON	2005/05/09 10:52
S67	186	"714"/724-738.ccls. and AC with test\$3	USPAT	OR	ON	2005/05/09 10:52
S66	873947	"714"/724-738.ccls. and AC wikh test\$3	USPAT	OR	ON	2005/05/09 10:52
S65	11	"714"/724-738.ccls. and two adj puls\$3 with generat\$3	USPAT	OR	ON	2005/05/09 10:52
S68	57	"714"/724-738.ccls. and AC adj test\$3	USPAT	OR	ON	2005/05/09 10:53
S70	493	AC adj test\$3	USPAT	OR	ON	2005/05/09 10:54
S72	4	((("6598192") or ("5524114") or ("6738921") or ("6671848"))).PN.	USPAT	OR	OFF	2005/05/09 10:55

S75	113	"714"/\$.ccls. and AC with test\$3 and scan and two	USPAT	OR	ON	2005/05/09 11:07
S74	582	AC with test\$3 and scan and two	USPAT	OR	ON	2005/05/09 11:07
S73	604	AC with test\$3 and scan	USPAT	OR	ON	2005/05/09 11:07
S71	107	AC adj test\$3 and scan	USPAT	OR	ON	2005/05/09 11:07
S77	31	"714"/\$.ccls. and AC with test\$3 with scan	USPAT	OR	ON	2005/05/09 11:09
S76	16	"714"/\$.ccls. and AC with test\$3 and scan and two with pulse	USPAT	OR	ON	2005/05/09 11:09
S79	1	("20030020451").PN.	US-PGPUB; USPAT	OR	OFF	2005/05/09 11:25
S1	8	OZAKI adj HIDEHARU.in.	USPAT	OR	OFF	2005/05/09 11:25
S82	127	714/724-738.ccls.and Circuit adj board adj test\$3	USPAT	OR	OFF	2005/05/09 14:23
S81	50	714/724-738.ccls.and Circuit adj board adj test	USPAT	OR	OFF	2005/05/09 14:23
S84	116411	714/724-738.ccls. and Circuit adj board adj test\$3 wit "IC"	USPAT	OR	OFF	2005/05/09 14:24
S83	116411	714/724-738.ccls.and Circuit adj board adj test\$3 wit "IC"	USPAT	OR	OFF	2005/05/09 14:24
S86	28	Circuit adj board adj test\$3 with "IC"	USPAT	OR	OFF	2005/05/09 14:25
S85	2	714/724-738.ccls. and Circuit adj board adj test\$3 with "IC"	USPAT	OR	OFF	2005/05/09 14:25
S89	84	324/763-765.ccls. and Circuit adj board with "IC"	USPAT	OR	OFF	2005/05/09 14:26
S91	157995	324/763-765.ccls. and Circuit adj board with test\$3 with "IC" wit socket	USPAT	OR	ON	2005/05/09 14:27
S90	1	324/763-765.ccls. and Circuit adj board adj test\$3 with "IC"	USPAT	OR	OFF	2005/05/09 14:27
S92	8	324/763-765.ccls. and Circuit adj board with test\$3 with "IC" with socket	USPAT	OR	ON	2005/05/09 14:30
S94	1	324/763-765.ccls. and wafer and printed adj Circuit adj board with "IC" with socket	USPAT	OR	ON	2005/05/09 14:31
S93	9	324/763-765.ccls. and printed adj Circuit adj board with "IC" with socket	USPAT	OR	ON	2005/05/09 14:31
S95	20	324/763-765.ccls. and wafer and printed adj Circuit adj board with socket	USPAT	OR	ON	2005/05/09 14:33
S96	12	714/724.ccls. and wafer and printed adj Circuit adj board with socket	USPAT	OR	ON	2005/05/09 14:34
S98	2	Teradyne.as. and kerveros	USPAT	OR	OFF	2005/05/09 14:41
S101	1	714/724.ccls. and "IC" with printed adj Circuit adj board with socket	USPAT	OR	ON	2005/05/09 15:02

S97	12	714/724.ccls. and wafer and printed adj Circuit adj board with socket	USPAT	OR	ON	2005/05/09 15:02
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